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DRIVING CIRCUIT FOR VACUUM FLUORESCENT DISPLAY

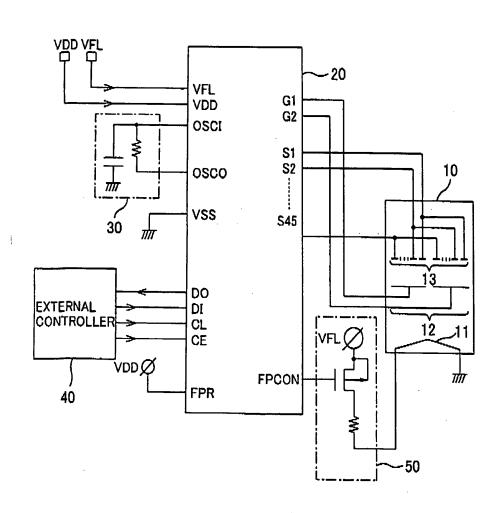
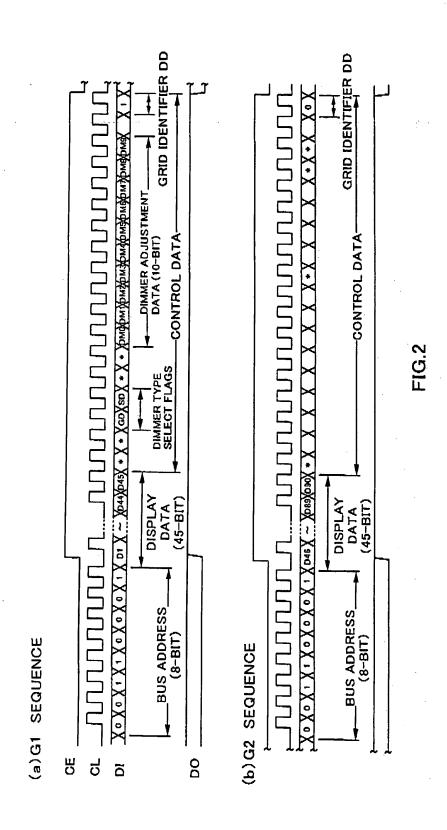
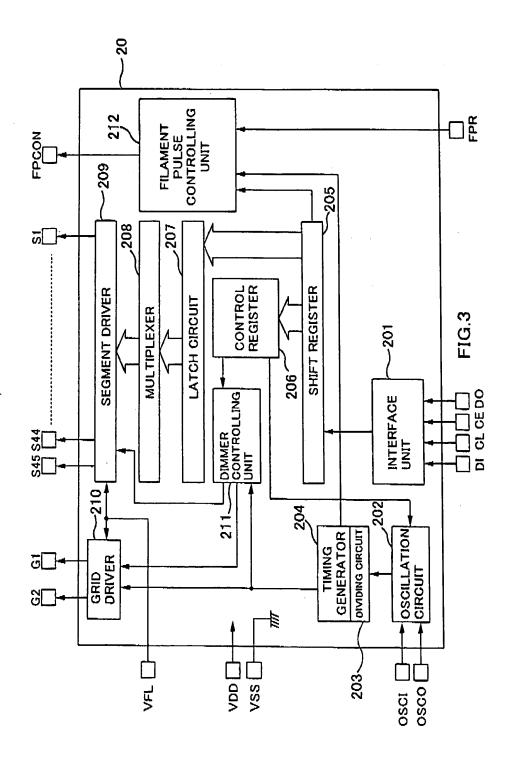


FIG.1



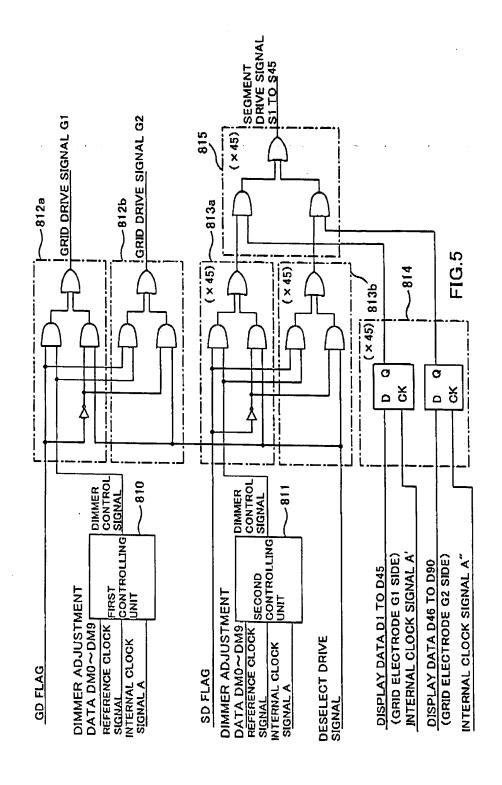
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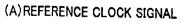


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DIMMER SELECT		OUTPUT STATUS	OUTPUT WAVEFORM
GD	SD	Gn AND Sn TERMINALS	Gn TERMINAL Sn TERMINAL
0	0	SETTING NOT ALLOWED	
0	1	DIMMER ADJUSTMENT FOR Sn OUTPUT ONLY	<;;;;;>
1	0	DIMMER ADJUSTMENT FOR Gn OUTPUT ONLY	<!--!!!-->
1	1	DIMMER ADJUSTMENT FOR BOTH Gn AND Sn OUTPUTS	<u><;;;;;></u>

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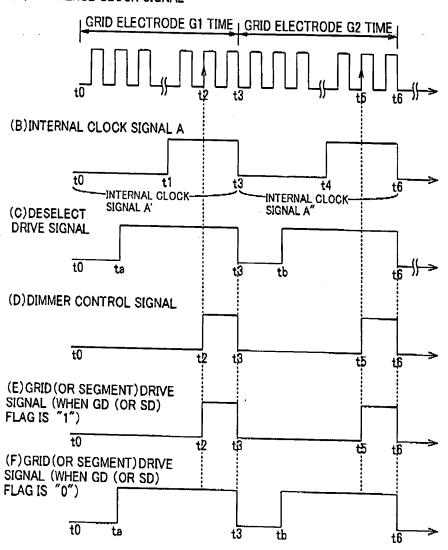


FIG.6

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			-DIMN	IER A	DJU	STME	NT D	ATA-			٦
	DM9	DM8	DM7	DM6	DM5	DM4	DM3	DM2	DM1	DM0	DIMMER VALUE (TW/T)
	0	0	0	0	0	0	0	0	0	0	0/1024
	-0	0	0	0	.0	0	0	0	0	1	1/1024
	0	0	0	0	0	0	0	0	1	0	2/1024
)	?									1	
	1	1	1	1	1	1	1	1	0	0	1020/1024
	1	1	1	1	1	1	1	1	0	1	1021/1024
	1	1	1	1	1	1	1	1 -	1	0	1022/1024
	1	1	1	1	1	1	1	1	1	t	SETTING NOT

FIG.7A

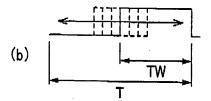
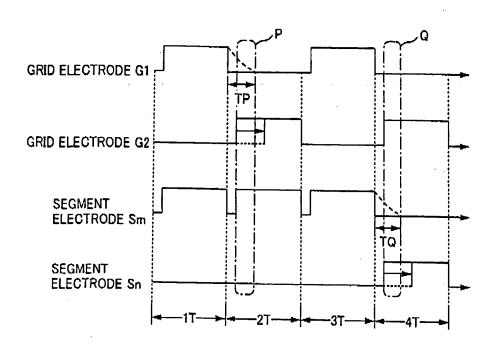


FIG.7B

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DRIVING CIRCUIT FOR VACUUM FLUORESCENT DISPLAY



(a)

FIG.8A

